





JC986 U.S. PTO <b>09/870955</b>  2705/30/01			
	Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

## U.S. UTILITY Patent Application

 O.I.P.E. SCANNED  Q.A. 	PATENT DATE
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APPLICATION NO. 09/870955	CONT/PRIOR F	CLASS 324 714	SUBCLASS 738	ART UNIT -2858 2133	EXAMINER <i>W. C. C.</i>
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APPLICANTS Klaus-Dieter Hilliges RSD *tones*

TITLE Integrated circuit tester with multi-port testing functionality

PTO-20  
12/99

## ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				<b>ISSUE FEE</b>	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.	_____ (Primary Examiner) _____ (Date)			<b>ISSUE BATCH NUMBER</b>	
_____ (Legal Instruments Examiner) _____ (Date)					
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